

# Bulk Heterojunction Polymer Memory Devices with Reduced Graphene Oxide as Electrodes

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As one of the emerging memory technologies, the organic memory (electrical bistable) device, with a typical configuration of organic active materials sandwiched between two electrodes, has attracted increasing interest due to its role in organic electronics comprising simple structure, low cost, large-capacity data storage, etc. Two important components of memory devices are the organic active materials and conducting electrodes. Recently, many kinds of organic active materials, including small molecules,<sup>1,2</sup> polymers,<sup>3</sup> redox metal complexes<sup>4</sup> and hybrid organic/inorganic nanocomposites,<sup>5–9</sup> have been used for memory devices, with Al, Cu, indium–tin oxide (ITO), and p- or n-doped Si as the most widely used electrodes. Among these memory devices, the polymer-based nanocomposites, including metal or metal oxide nanoparticles<sup>6,7</sup> and carbon-based nanomaterials,<sup>5,8,9</sup> have shown particular interest as nanomaterials normally act as the charge acceptors to realize the memory effect. Although the poly(3-hexyl thiophene) (P3HT) and methanofullerene [6,6]-phenyl C61-butyric acid methyl ester (PCBM) bulk-heterojunction (BHJ) composites, a promising system of electron donors and acceptors, were used for the energy conversion,<sup>10,11</sup> few studies on the electrical bistable behaviors of this BHJ composites have been reported until now.

On the other hand, a proper substitute for ITO and other metal electrodes is required during the fabrication of memory devices, due to the shortcomings of normally used electrodes, such as the brittle nature, high cost, and limit amount of the ele-

**ABSTRACT** A unique device structure with a configuration of reduced graphene oxide (rGO) /P3HT:PCBM/Al has been designed for the polymer nonvolatile memory device. The current–voltage ( $I$ – $V$ ) characteristics of the fabricated device showed the electrical bistability with a write-once-read-many-times (WORM) memory effect. The memory device exhibits a high ON/OFF ratio ( $10^4$ – $10^5$ ) and low switching threshold voltage (0.5–1.2 V), which are dependent on the sheet resistance of rGO electrode. Our experimental results confirm that the carrier transport mechanisms in the OFF and ON states are dominated by the thermionic emission current and ohmic current, respectively. The polarization of PCBM domains and the localized internal electrical field formed among the adjacent domains are proposed to explain the electrical transition of the memory device.

**KEYWORDS:** polymer memory · bulk heterojunction · write-once-read-many-times · reduced graphene oxide · sheet resistance

ment in the earth, etc. Meanwhile, the work function, sheet resistance, and chemical stability of electrode materials greatly affect the electrical properties of memory devices.<sup>12–15</sup> For example, the current–voltage ( $I$ – $V$ ) characteristics of memory devices are affected by the charge injection barrier, which is dependent on the work functions of electrodes and the energy levels of organic materials. Therefore, how to choose the appropriate electrode materials is very important in the design of memory devices. If the flexible conducting polymer electrode is used, its high sheet resistance normally induces a high switching voltage in the memory device.<sup>13</sup> In addition, the chemical stability of electrodes will directly affect the  $I$ – $V$  properties and memory effects of devices. For example, the copper-doping in the organic active layer during deposition can induce a hysteresis phenomenon, which, however, was not observed in Al electrodes because of no Al nanoparticles penetrating into the organic materials.<sup>16,17</sup>

Graphene is a promising conductive material due to its extremely high carrier

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Received for review April 25, 2010 and accepted June 03, 2010.

Published online June 11, 2010. 10.1021/nn100877s

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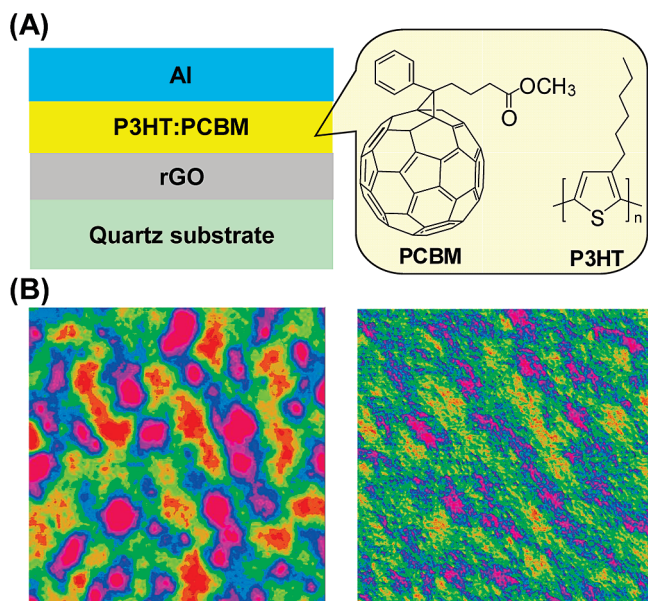


Figure 1. (A) Schematic diagram of the configuration of memory device (rGO/P3HT:PCBM/Al) and the chemical structures of P3HT and PCBM. (B) Tapping-mode AFM topographic (left) and phase (right) images of P3HT:PCBM layer on rGO film. Figure size =  $2.0 \times 2.0 \mu\text{m}^2$ . Z scale for topographic (left) and phase (right) images is 20 nm and  $20^\circ$ , respectively.

mobility, high chemical stability, and flexibility. Various approaches including micromechanical cleavage,<sup>18</sup> epitaxial growth,<sup>19,20</sup> solvothermal synthesis,<sup>21</sup> and liquid-phase exfoliation,<sup>22–24</sup> have been proposed to prepare graphene, but these methods face the challenges of low cost, high yield, and large-amount production. Currently, the graphene oxide (GO) reduced by thermal annealing, which is so far one of the most efficient and effective methods to produce reduced GO (rGO), is used as electrodes in solar cells, organic light-emitting diodes, and electronic devices.<sup>25–28</sup> In this contribution, we demonstrate that the rGO film can serve as a conductive electrode in the BHJ polymer memory device, which exhibits a write-once-read-many-times (WORM)

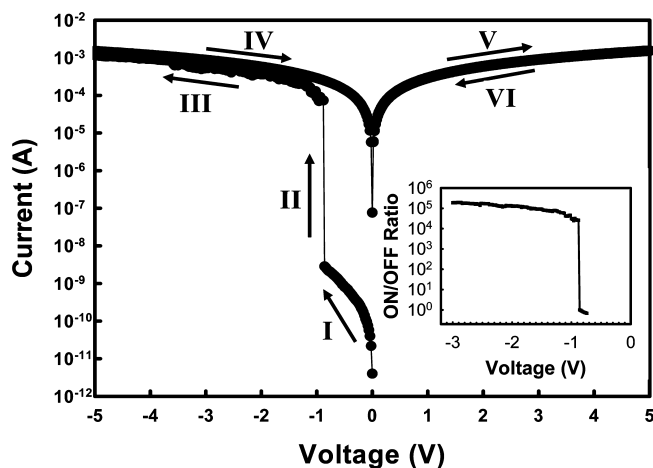


Figure 2. The  $I$ – $V$  characteristics of the rGO/P3HT:PCBM/Al device. The sheet resistance of rGO electrode is  $\sim 2.4 \text{ k}\Omega/\text{sq}$  (device 2 in Table 1). Inset: the ON/OFF ratio as a function of applied voltage in the negative sweep.

memory effect. In addition, the carrier transport mechanism of the memory device is proposed, and its electrical transition from a low conductivity state (OFF state) to a high conductivity state (ON state) is discussed.

The polymer memory device with the BHJ active layer and rGO electrode was fabricated on quartz. Briefly, the GO thin film was spin-coated on the pre-cleaned quartz. Its thickness was adjusted by the spin-coating condition. To improve the electrical conductivity of the electrode, GO film was reduced by the high temperature annealing and the conducting rGO film was obtained. Subsequently, a P3HT:PCBM functional layer was spin-coated onto the top of rGO film to realize the memory effect. Finally, a top Al electrode was coated by thermal evaporation under vacuum. Figure 1A shows the schematic diagram of the fabricated memory, referred to as rGO/P3HT:PCBM/Al, and the chemical structures of P3HT and PCBM. Figure 1B shows the AFM images of the obtained P3HT:PCBM film, where the topography and phase images clearly reveal the formation of aggregated domains of PCBM in the P3HT matrix after thermal annealing treatment, which is consistent with the previous report.<sup>29</sup>

The memory effect was observed from the characteristic  $I$ – $V$  curves of the rGO/P3HT:PCBM/Al device (Figure 2). Initially, the conductivity of the as-fabricated device is low (defined as the OFF state). The current increased progressively after applying the negative bias (stage I), varying in a range of approximately  $10^{-12}$  to  $10^{-9}$  A. When a switching threshold voltage of  $-0.9$  V was applied (stage II), the current increases dramatically from  $10^{-9}$  to  $10^{-5}$  A, indicating the transition of the diode from a low conductivity state (OFF state) to a high conductivity state (ON state), with an ON/OFF current ratio of  $10^4$  (inset in Figure 1). This transition from the OFF to ON state is equivalent to the “writing” process, and the high ON/OFF current ratio promises a low misreading probability in the memory operation.<sup>30</sup> After the electronic transition, the device remains in the ON state during the subsequent negative voltage scan (stages III and IV), even if the power is turned off. This promises the nonvolatile nature of the memory effect. The current in the ON state is in the range of  $10^{-7}$ – $10^{-3}$  A with the sweeping voltages from  $-5$  to  $5$  V. One of the most impressive features of the fabricated device is that the high conductivity does not go back to the low one by applying a reverse voltage scan (stage V), and a similar phenomenon is observed in the subsequent scan (stage VI). Therefore, this device exhibits the WORM type memory effect. Note that the role of PCBM molecules is very important in the fabricated memory device. Without PCBM, the  $I$ – $V$  curves do not exhibit the electrical bistable phenomenon, indicating that the WORM effect of the BHJ device is attributed to the existence of PCBM.

To clarify the effect of sheet resistance of rGO film on the device performance, we have characterized the

**TABLE 1. Sheet Resistances of rGO Films and the Performance of Fabricated Polymer Memory Devices. The  $I-V$  Curves of Device 2 Are Shown in Figure 2**

device	sheet resistance (k $\Omega$ /sq)	switching voltage (V)	current in OFF state (A)	current in ON state (A)	ON/OFF ratio
1	~10.0	1.2	$5.1 \times 10^{-9}$	$6.2 \times 10^{-5}$	$1.2 \times 10^4$
2	~2.4	0.9	$2.8 \times 10^{-9}$	$7.3 \times 10^{-5}$	$2.2 \times 10^4$
3	~0.3	0.5	$5.7 \times 10^{-10}$	$2.2 \times 10^{-4}$	$4 \times 10^5$

$I-V$  properties of the rGO/P3HT:PCBM/Al devices with different rGO electrodes. It was found that the switching behavior of all memory devices is similar to that in Figure 2, and all devices exhibited the WORM effect. On the basis of our experimental results, the summary of sheet resistance of rGO film, switching voltage, current in ON and OFF state, and ON/OFF ratio of the measured devices is listed in Table 1. Obviously, the switching voltage decreases with the decrease of sheet resistance of rGO film. Normally, the electrode with a high sheet resistance will lower the carrier injection and increase the switching voltage in organic memories.<sup>13</sup> In our study, the lower sheet resistance of rGO film enhances the carrier injection efficiency from rGO electrode to BHJ. Therefore, the switching threshold voltage increases in the device with a higher sheet resistance rGO electrode. This phenomenon might be attributed to the fact that the current in OFF state is limited under the low injection efficiency. Furthermore, the increase of ON state current and decrease of OFF state current result in the increased ON/OFF current ratio, which is particularly obvious in the devices with a lower sheet resistance of rGO electrode (Table 1).

To understand the carrier transport mechanism in the WORM memory device, the experimental  $I-V$  data and fitted data in the ON and OFF states are studied for device 2 (Table 1), Figure 3. In the OFF state, the plot of  $\ln(I)$  vs  $V^{1/2}$  in the sweeping voltage ranging from 0.06 to 0.9 V before the electrical transition can be fitted to a straight line with eq 1,<sup>31</sup> Figure 3A.

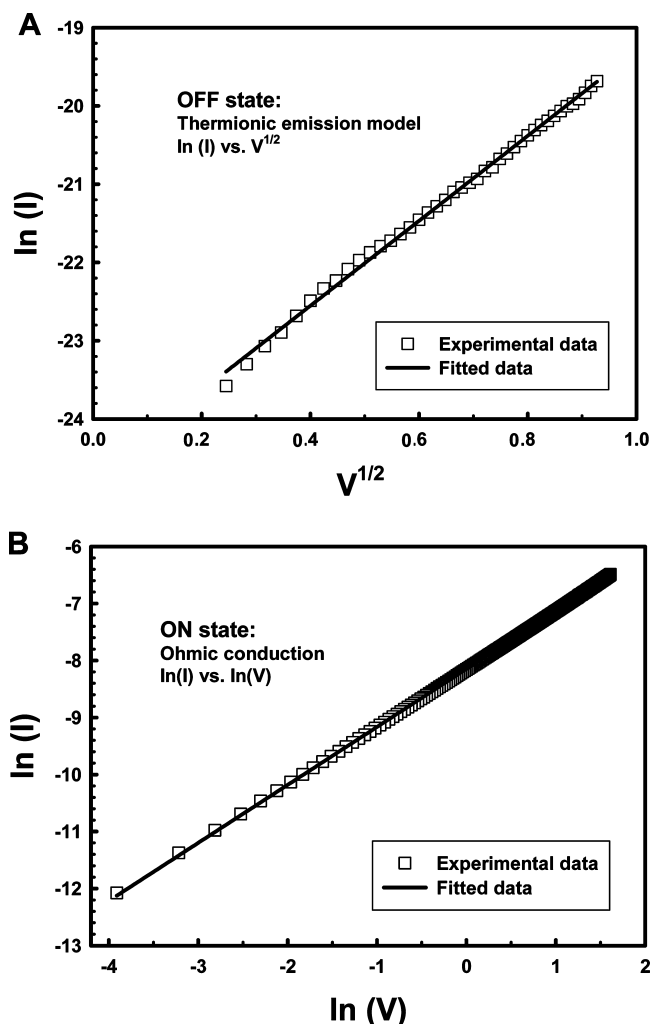
$$I \propto AT^2 \exp\left[\frac{-(\phi - q\sqrt{qV/4d\pi\epsilon})}{kT}\right] \quad (1)$$

where  $I$  and  $V$  are the device current and applied voltage, respectively;  $A$ ,  $T$ ,  $k$ ,  $\phi$ ,  $q$ ,  $d$ ,  $\pi$ , and  $\epsilon$  are the Richardson constant, temperature, Boltzmann constant, Schottky energy barrier, electron charge, BHJ film thickness, ratio of circumference to diameter, and electric constant, respectively. Such a linearity characteristic suggests that the carrier transport mechanism in the OFF state is dominated by the thermionic emission, and the conduction mechanism is dominated by the charge injection from electrodes.

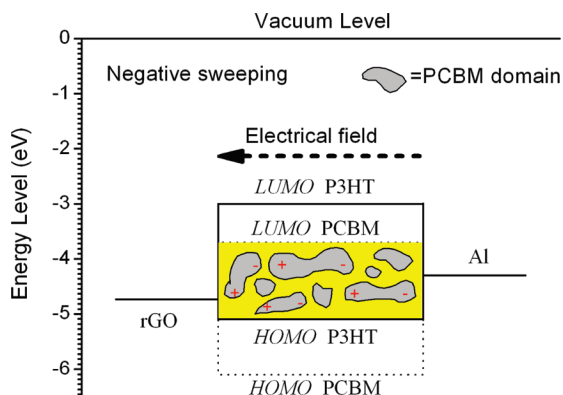
In the ON state from  $-5$  to  $-0.02$  V after the conductive switching, the experimental data of the rGO/P3HT:PCBM/Al device, shown in the plot of  $\ln(I)$  vs  $\ln(V)$  (Figure 3B), were fitted with eq 2.<sup>31</sup>

$$I \propto V \exp\left(\frac{-\Delta E_{ae}}{kT}\right) \quad (2)$$

where  $I$  and  $V$  are the device current and applied voltage, respectively;  $\Delta E_{ae}$ ,  $T$ , and  $k$  are the activation energy of electron, temperature, and Boltzmann constant, respectively. The obtained slope (1.02) is nearly equal to 1, indicating that the ohmic conduction dominates the carrier transport process.<sup>31</sup> Since the  $I-V$  behaviors for the positive (stage V) and reverse (stage VI) voltage sweeping between 0 and 5 V are very similar to those (stage IV) from 0 to  $-5$  V, as shown in Figure 2, the carrier transport mechanism in the ON state is the ohmic process. Meanwhile, the ohmic process promises the



**Figure 3.** Experimental results and theoretical fitted data of the  $I-V$  curves for the rGO/P3HT:PCBM/Al device (*i.e.*, device 2 in Table 1) in (A) OFF state (low conductivity) and (B) ON state (high conductivity).



**Figure 4.** The energy band diagrams of the rGO/P3HT:PCBM/Al device and a schematic illustration of the polarization of PCBM domains in P3HT matrix under an applied electrical field.

absence of captured carriers due to the trap-free polymer, fullerene devices,<sup>32</sup> which is quite different from the hybrid organic/inorganic nanocomposites and PCBM-based memories.<sup>8,33–35</sup> In these memory devices, nanocomposites normally act as the charge traps to realize the electrical switching and memory effect. For example, the dominated factor to affect the ON state current is probably the space-charge-limited-current for the polystyrene–Au nanoparticle system,<sup>33</sup> Poole–Frenkel emission for the polystyrene–PCBM system,<sup>8</sup> and Fowler–Nordheim tunneling for polyimide–Ni<sub>1-x</sub>Fe<sub>x</sub> nanoparticle system.<sup>34</sup>

On the basis of the aforementioned discussion, we propose that the electrical switching behavior is attributed to the field-induced polarization of PCBM domains. Figure 4 shows the energy band diagrams of the fabricated rGO/P3HT:PCBM/Al device and a schematic illustration of polarization in PCBM domains in P3HT matrix. The work function of rGO film measured by Kelvin probe force microscopy (KPFM) is  $-4.7$  eV, which is comparable to the values ( $-4.88$  eV) of chemically reduced GO film.<sup>36</sup> The work function of Al is  $-4.3$  eV. The highest occupied molecular orbital (HOMO) and lowest unoccupied molecular orbital (LUMO) are  $-5.2$  and  $-3.0$  eV for P3HT and  $-6.1$  and  $-3.7$  eV for PCBM, respectively. This indicates a large electron-injection barrier (1.0 eV) and hole-injection barrier (0.9 eV) under the applied negative voltage. The large barrier results in the low injection efficiency at the first negative sweeping, resulting in the thermionic emission in the OFF state. The thermionic emission domi-

nates the carrier transport process until the applied voltage approaches the switching voltage. Normally, the thermal annealing can introduce the formation of PCBM domains,<sup>37–39</sup> resulting in the discontinuity of PCBM-rich phase in matrix. Therefore, the P3HT:PCBM composites are illustrated as two dielectric phases with a dielectric constant of 3.9 for PCBM<sup>40</sup> and 3 for P3HT.<sup>41</sup> The formed PCBM domains in the P3HT matrix can be polarized under an applied electrical field.<sup>42</sup> The phenomenon should be more distinct under the low charge injection efficiency. The polarization of PCBM domains leads to the increase of the localized internal electrical field among the adjacent domains and induces the conductive switching.<sup>43</sup> Meanwhile, the polarized states and the localized internal electrical fields are maintained, even when the power is turned off due to the nonvolatile properties of devices. In the previous reports, the switching behavior was explained by the metal filament model for P3HT-based memory devices<sup>14</sup> and the field-induced charge transfer for PCBM-based memory devices,<sup>8,35</sup> but the former memory exhibits the erasing operation under a reverse bias applied and a high ON state current of  $10^{-2}$ – $10^{-1}$  A, and the latter memory shows the erasing operation due to the detrapping of trapped charges under an external electrical field. Therefore, the models of metal filament and field-induced charge transfer cannot be used to explain our results.

In summary, an electrical bistable device with the configuration of rGO/P3HT:PCBM/Al has been demonstrated. The  $I$ – $V$  curves for the devices show the electrical bistable behavior, a WORM memory effect, and the dependence of switching threshold voltage on the sheet resistance of rGO electrodes. The carrier transport mechanism based on the experimental and fitted data of  $I$ – $V$  curves indicates an electrical transition from the charge injection limited current in the OFF state to the ohmic current in the ON state. Such transition might be attributed to the polarization of PCBM domains and the formation of a localized internal electrical field among the adjacent domains. Because of the nonvolatile and inerasable features, the WORM memory devices have potential applications in the archival storages and databases, in which the information has to be reliably kept and capable of being stored permanently. Meanwhile, WORM memory could also be used as electronic labels and radio frequency identification (RFID).

## MATERIALS AND METHODS

**Preparation of Reduced Graphene Oxide (rGO) Film.** Graphene oxide (GO) was prepared by the modified Hummers method.<sup>44,45</sup> GO powder was obtained by drying the GO suspension in vacuum for a week. Exfoliated GO was dispersed in methanol by ultrasonication. The quartz was sonicated in DI-water with subsequent cleaning in piranha solution ( $\text{H}_2\text{SO}_4$ :30 wt %  $\text{H}_2\text{O}_2 = 3:1$ ) at  $110^\circ\text{C}$  for 1 h. After the cleaned quartz was thoroughly rinsed with Milli-Q water and dried with  $\text{N}_2$  gas, the dispersed GO solution

was spin-coated on its surface at 4000 rpm. The thickness of GO film can be adjusted by controlling the spin-coating conditions. The produced GO film was then annealed at  $1000^\circ\text{C}$  under the mixture of  $\text{Ar}_2$  and  $\text{H}_2$  for 2 h to obtain the conductive rGO film.

**Fabrication of Polymer Memory Devices.** Polymer memory devices were fabricated on the rGO film-coated quartz substrates. The active layer is the bulk heterojunction (BHJ) composites of P3HT and PCBM ( $w/w = 1:1$ ). First, the BHJ composites were spin-coated onto the top of rGO film at 700 rpm and then annealed

at 150 °C for 15 min under N<sub>2</sub> atmosphere, producing a 200 nm BHJ film. Then a 60 nm top Al electrode, with an active area of ~6 mm<sup>2</sup>, was thermally evaporated under the vacuum of ~10<sup>-6</sup> mbar. Before measurement, the as-fabricated device was annealed again at 150 °C for 15 min under N<sub>2</sub> atmosphere.

**Characterization.** The AFM images were obtained by using a Dimension 3100 (Veeco, CA) in tapping mode with a Si tip (resonance frequency: 320 kHz; spring constant: 42 N m<sup>-1</sup>) at a scanning rate of 1 Hz. The work function was measured by KPFM (KP technology Ltd., Caithness, UK) in air based on the contact potential difference between the sample and the reference Au electrodes. The *I*–*V* measurements were performed by using a Hewlett-Packard 4156B semiconductor parameter analyzer under ambient conditions. The sheet resistance was measured by a four-probe instrument.

**Acknowledgment.** The authors gratefully acknowledge the financial support from the “973” project (2009CB930600), NNSFC (Grants 20704023, 60876010, 60706017, and 20774043), the Key Project of Chinese Ministry of Education (No. 208050, 707032, NCET-07-0446), the NSF of Jiangsu Province (Grants 07KJB150082, BK2008053, 08KJB510013, SJ209003and TJ207035), the Research Fund for Postgraduate Innovation Project of Jiangsu Province (CX08B\_083Z) and STTP (No. 2009120). H.Z. acknowledges the following support: AcRF Tier 1 (RG 20/07) from MOE, CRP (NRF-CRP2-2007-01) from NRF, A\*STAR SERC Grants (No. 092 101 0064) from A\*STAR, and the Centre for Biomimetic Sensor Science at NTU in Singapore.

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